

5-27-5 2183 Ifw

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

In re application of:	Enric Musoll Mario D. Nemirovsky
Serial No.:	09/616385
Filed:	07/14/00
Docket:	MIPS.0165-00-US
For:	METHODS AND APPARATUS FOR IMPROVING FETCHING AND DISPATCH OF INSTRUCTIONS IN MULTITHREADED PROCESSORS

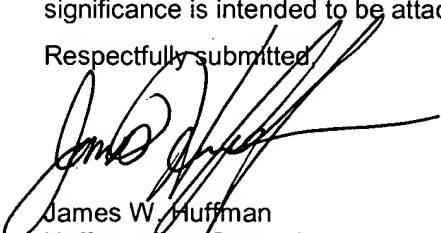
Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the Examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims and return an initialed copy of each form to the undersigned.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 C.F.R. § 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR § 1.56(b) or is available as a reference under 35 U.S.C. § 102 *et seq.* Applicant reserves the right to swear behind or otherwise disprove the alleged "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98, and the Manual of Patent Examining Procedures § 609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

A copy of each document is enclosed. Some of the documents may have markings thereon. No significance is intended to be attached to the markings.

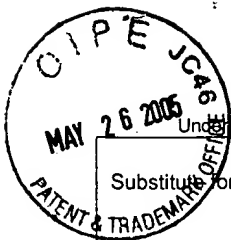
Respectfully submitted,


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Substitute for form 1449-PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	09/616385
Filing Date	07/14/00
First Named Inventor	Enric Musoll
Art Unit	2183
Examiner Name	Eric Coleman
Attorney Docket Number	MIPS.0165-00-US

Sheet

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of

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Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		6105127	Kozo et al	08-15-2000	
		09/592106	Melvin et al.	filed 06-12-2000	
		09/595776	Musoll et al.	filed 06-16-2000	
		09/616385	Musoll et al.	filed 07-14-2000	
		09/629805	Nemirovsky et al.	filed 07-31-2000	
		09/312302	Nemirovsky et al.	filed 05-15-1999	
		09/273810	Nemirovsky et al.	filed 03-22-1999	
		20020062435	Nemirovsky et al.	05-23-2002	
		09/240012	Nemirovsky et al.	filed 01-27-1999	
		6356996	Adams, Phillip M.	03-12-2002	
		6260077	Rangarajan et al.	07-10-2001	
		20030084269	Drysdale et al.	05-01-2003	
		6493749	Paxhia et al.	12-10-2002	
		6430593	Lindsley, Brett Louis	08-06-2002	
		6442675	Derrick et al.	08-27-2002	
		6192384	Dally et al.	02-20-2001	
		4197579	Forsman et al.	04-08-1980	

Examiner Signature		Date Considered	
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This collection of information is required by 27 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449-PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	09/616385
				Filing Date	07/14/00
				First Named Inventor	Enric Musoll
				Art Unit	2183
				Examiner Name	Eric Coleman
Sheet	2	of	3	Attorney Docket Number	MIPS.0165-00-US

FOREIGN PATENT DOCUMENTS						
Examiner Initials *	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear
		Country Code ³	Number ⁴ Kind Code ⁵ (if known)			
	BA	JP10207717A2 (Translation of Relevant Parts and Abstract provided)		08-07-1998	Matsuzaki et al.	Yes
	BB	JP1011301A2 (Translation of Relevant Parts and Abstract provided)		01-16-1998	Imai et al.	Yes
	BC	JP09506752A2 (Translation of Relevant Parts and Abstract provided)		06-30-1997	Dobbins et al.	Yes
	BD	JP10124316A2 (Translation of Filing Particulars and Abstract provided)		05-15-1998	Kimura et al.	Yes
	BE	JP2103630 (Translation of Relevant Parts and Abstract Provided)		10-12-1988	Makita et al.	Yes
	BF	JP4335431 (Translation of Relevant Parts and Abstract Provided)		11-24-1992	Sakai, Noriaki	Yes
	BG	JP63254530 (Translation of Relevant Parts and Abstract Provided)		10-21-1988	Tanitsu, Eiji	Yes
	BH	JP546379 (Translation of Relevant Parts and Abstract Provided)		02-26-1993	Okazaki et al.	Yes
	BI	EP0827071A2		03-04-1998	Kimura et al.	
	BJ	WO9427216A1		11-24-1994	Keckler et al.	
	BK	EP0806730A2		11-12-1997	Zolnowsky, John E.	
	BL	WO0023891A1		04-27-2000	Lundstrom et al.	

Examiner Signature		Date Considered	
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				Examiner Name	Eric Coleman
Sheet	3	of	3	Attorney Docket Number	MIPS.0165-00-US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	CUI ET AL., Parallel Replacement Mechanism for MultiThread, Advances in Parallel and Distributed Computing, 1997. Proceedings, IEEE, March 21, 1977, pp. 338-344	
	CB	TULLSEN ET AL., Supporting Fine-Grained Synchronization on a Simultaneous Multithreading Processor, USCD CSE Technical Report CS 98-587, June 1998, all pages, US	
	CC	THEKKATH ET AL. The Effectiveness of Multiple Hardware Contexts. In Proceedings of the 6th International Conference on Architectural Support for Programming Languages and Operating Systems, pages 328--337, San Jose, CA, October 1994. ACM	
	CD	MCFARLING, SCOTT. Combining Branch Predictors. WRL Technical Note TN-36. June 1993. Pages 11-12. Digital Western Research Laboratory. Palo Alto, CA, US	

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